

	Type	Hits	Search, Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	14	memory with module and circuit with board with electrical and volatile with memory and radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:07		
2	BRS	14	5739791.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:23		
3	BRS	52	circuit with board with electrical and volatile with memory and radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 09:52		
4	BRS	6	circuit with board with electrical and dynamic with volatile with memory and radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:08		
5	BRS	1805	702/62-65,80,117,186,for.110,for.155.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:12		
6	BRS	564	710/64,74,106.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:23		
7	BRS	2480	343/870,905,906,702,841.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:14		
8	BRS	156	455/90.3.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:24		
9	BRS	3622	stor\$3 with memory with module with information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:27		
10	BRS	5	stor\$3 with memory with module with information and circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:29		
11	BRS	1	transmit\$3 with stor\$3 with memory with module with information and circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:31		

	Type	Hits	Search, Text	DBs	Time Stamp	Comments	Error Definition
12	BRS	1	transmit\$3 with memory with information with circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:32		
13	BRS	9	transmit\$3 with memory with information and circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:33		
14	BRS	1	transmit\$3 with memory with module with circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:35		
15	BRS	1	radio with transmit\$3 and memory with module with circuit with board with electrical with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:36		
16	BRS	8	memory with module\$1 and circuit with board with electr\$4 with terminal\$1 and radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:41		
17	BRS	9	memory with module\$1 with radio with transmit\$3 and circuit with board with electr\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:08		
18	BRS	3	memory with module\$1 with radio with transmit\$3 and circuit with board with electr\$4 with terminal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:54		
19	BRS	101	memory with module\$1 and circuit with board with electr\$4 and radio with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 10:59		
20	BRS	31	memory with module\$1 and circuit with board with electr\$4 and radio with transmit\$3 and volatile with non-volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:09		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
21	BRS	52	memory with module\$1 and circuit with board with electcr\$4 and radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:14		
22	BRS	16	(343/870,905,906,702,841.ccls.) and memory with module\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:15		
23	BRS	0	(343/870,905,906,702,841.ccls.) and memory with module\$1 with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:15		
24	BRS	3	(343/870,905,906,702,841.ccls.) and memory with module\$1 with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:15		
25	BRS	20	(343/870,905,906,702,841.ccls.) and memory with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:18		
26	BRS	4	(343/\$.ccls.) and memory with module\$1 with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:25		
27	BRS	847	memory adj module\$1 with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:22		
28	BRS	10	(non adjl volatile or non-volatile) with memory adj module\$1 with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:23		
29	BRS	417	(343/\$.ccls. or 710/\$.ccls. or 455/\$.ccls.) and memory with module\$1 with transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:27		
30	BRS	67	(343/\$.ccls. or 710/\$.ccls. or 455/\$.ccls.) and memory with module\$1 with transmit\$3 and circuit with board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:28		
31	BRS	31	(343/\$.ccls. or 710/\$.ccls. or 455/\$.ccls.) and memory with module\$1 with transmit\$3 and circuit with board and memory with (non-volatile or (non adjl volatile) or dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 11:31		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
32	BRS	6	(343/\$.cccls. or 710/\$.cccls. or 455/\$.cccls.) and memory with module\$1 with transmit\$3 and circuit with board with electric\$2 and memory with (non-volatile or (non adj1 volatile) or dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 12:05		
33	BRS	5	(343/\$.cccls. or 710/\$.cccls. or 455/\$.cccls.) and memory with module\$1 with information and transmit\$3 and circuit with board with electric\$2 and memory with (non-volatile or (non adj1 volatile) or dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 12:14		
34	BRS	2	(343/\$.cccls. or 710/\$.cccls. or 455/\$.cccls.) and memory with module\$1 and radio with transmit\$3 and circuit with board with electric\$2 and memory with (non-volatile or (non adj1 volatile) or dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 12:16		
35	BRS	0	"I.sup.2C adj bus"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:10		
36	BRS	191	"I.sup.2C bus"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:17		
37	BRS	1	"I.sup.2C bus" and memory with radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:27		
38	BRS	17	"I.sup.2C bus" and memory with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:22		
39	BRS	7	"I.sup.2C bus" and radio with transmitter and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:27		

	Type	Hits	Search. Text	DBs	Time Stamp	Comments	Error Definition
40	BRS	22	memory with radic with transmitter and (sdram or (synchronous adj1 dynamic adj1 random adj1 access adj1 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:32		
41	BRS	0	memory with radio with transmitter with (sdram or (synchronous adj1 dynamic adj1 random adj1 access adj1 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:29		
42	BRS	14	memory with module\$1 and radio with transmitter and (sdram or (synchronous adj1 dynamic adj1 random adj1 access adj1 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:58		
43	BRS	100	memory with module\$1 and radio with transmitter with transceiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:04		
44	BRS	3	memory with module\$1 with radio with transmitter with transceiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:09		
45	BRS	1	memory with module\$1 and radio with transmitter with transceiver and (SIMMs or (Single adj1 In-line adj1 Memory adj1 Modules))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:06		
46	BRS	3	memory with module\$1 with radio with transmitter with transceiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:06		
47	BRS	9	memory adj1 module\$1 and radio with transmitter with transceiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:24		
48	BRS	9	memory adj module\$1 and transmitter with radio with transceiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:52		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
49	BRS	4	memory adj module\$1 and processor with transmitter with radio and signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:01		
50	BRS	2	memory adj module\$1 and transmitter with radio and processor with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:04		
51	BRS	0	memory with module\$1 and transmit\$3 with radio and processor with signal with strength with relay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:05		
52	BRS	33	memory with module\$1 and transmit\$3 with radio and processor with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:16		
53	BRS	6	memory with module\$1 with transmit\$3 with radio and (microprocessor or processor) with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:20		
54	BRS	14	memory with module\$1 with transmit\$3 with radio and signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:27		
55	BRS	14	memory adj module\$1 and radio with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 16:49		
56	BRS	25	processor with signal with strength with radio with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 09:58		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
57	BRS	2	signal with strength\$1 with propagation with delay\$1 with radio with signal\$1 with locat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 09:35		
58	BRS	15	signal with strength\$1 with radio with signal\$1 with locat\$3 with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 09:41		
59	BRS	25	processor with signal with strength\$1 with radio with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:11		
60	BRS	3	memory with module\$1 with processor with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:00		
61	BRS	1	memory with module\$1 with processor with rssi	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:01		
62	BRS	2	memory adj module\$1 and (microprocessor or processor) with rssi	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:05		
63	BRS	1	memory adj module\$1 and (microprocessor or processor) with signal with strength with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:06		
64	BRS	1	((memory adj module\$1) or dimm\$1) and (microprocessor or processor) with signal with strength with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:07		
65	BRS	0	processor with signal with propagation with radio with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:11		
66	BRS	4	processor with signal with propagation with radio with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:45		
67	BRS	4	processor with signal with strength with radio with transmitter\$1 with location	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:33		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
68	BRS	25	processor with signal with strength with radio with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:30		
69	BRS	4	processor with signal with strength with radio with transmitter\$1 with (location or position)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:36		
70	BRS	8	processor with signal with strength with radio with transmitter\$1 with (locat\$3 or posit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:42		
71	BRS	242	processor with location with memory with module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:42		
72	BRS	69	processor with location with memory adj1 module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:44		
73	BRS	5	(microprocessor\$1 processor\$1) with (locat\$4 or posit\$4) with memory adj1 module\$1 and transmitter\$1 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:55		
74	BRS	22	(microprocessor\$1 processor\$1) with signal with strength with radio with transmitter and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:50		
75	BRS	7	(microprocessor\$1 processor\$1) with signal with strength with radio with transmitter with (locat\$3 or posit\$3) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 10:57		
76	IS&R	2	("5293645").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:09		
77	BRS	138	memory with radio with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:50		
78	BRS	5	memory with module\$1 with radio with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:47		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
79	BRS	79	memory with radio with transmitters with receiver\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:30		
80	BRS	435	370/514.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 11:58		
81	BRS	369	rds adj1 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:39		
82	IS&R	2	("4833728").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:39		
83	BRS	3	(microprocessor\$1 processor\$1) with signal with strength with radio with transmitters and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:50		
84	BRS	87	(microprocessor\$1 processor\$1) with signal with strength and radio with transmitters and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:51		
85	BRS	88	(microprocessor\$1 processor\$1) with signal\$1 with strength and radio with transmitters and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:04		
86	BRS	7	(microprocessor\$1 processor\$1) with signal\$1 with strength and radio with transmitters and memory and (self-test or boot\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:04		
87	BRS	4	(microprocessor\$1 processor\$1) with signal\$1 with strength and radio with transmitters and memory and boot\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:09		
88	BRS	53	radio with transmitter\$1 with memory with module\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:34		
89	BRS	0	radio with transeiver\$1 with memory with module\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:35		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
90	BRS	97	radio with transceiver\$1 with memory with module\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:35		
91	BRS	8	radio with transceiver\$1 with memory adj module\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:08		
92	BRS	115	memory adj module\$1 adj locat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:14		
93	BRS	38	memory adj module\$1 adj location	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:23		
94	BRS	2	memory adj module\$1 adj location and transmitter with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:19		
95	BRS	2	memory adj module\$1 adj (position\$1 or location\$1) and transmit\$3 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:20		
96	BRS	1759	memory adj (position\$1 or location\$1) and transmit\$3 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:21		
97	BRS	30	memory with (bit or address) with (position\$1 or location\$1) with transmit\$3 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:22		
98	BRS	33	memory adj (position\$1 or location\$1) with transmit\$3 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:23		
99	BRS	3	memory adj module\$1 adj location and radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:24		
100	BRS	90	memory adj location with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:24		
101	BRS	34	memory adj location with radio with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:30		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
102	BRS	4	memory adj location with radio with signal with strength	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:44		
103	BRS	4	memory with signal\$1 with strength\$1 with propagation with delay\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 14:45		
104	BRS	24	memory with signal\$1 with strength\$1 with delay\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 15:20		
105	BRS	4	memory with signal\$1 with strength\$1 with propagat\$3 with delay\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 15:40		
106	BRS	48	memory and signal\$1 with strength\$1 with propagat\$3 with delay\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 15:28		
107	BRS	191	signal\$1 with strength\$1 with memory with (position\$1 or location\$1 or address\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 15:43		
108	BRS	20	signal\$1 with strength\$1 with radio with memory with (position\$1 or location\$1 or address\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:17		
109	BRS	8	determin\$3 with memory with (position\$1 or location\$1 or address\$2 or bit) with signal\$1 with strength\$1 with radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:01		
110	BRS	388	identity near1 memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:01		
111	BRS	2	identity near1 memory adj slot	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:07		
112	BRS	22	identity near1 memory and radio with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:14		
113	BRS	1	identity near1 memory with slot and radio with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:14		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
114	BRS	428	determin\$3 adj memory adj location\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:18		
115	BRS	923	determin\$3 adj memory adj (position\$1 or address\$2 or location\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:19		
116	BRS	2	determin\$3 adj memory adj (position\$1 or address\$2 or location\$1) and radio with signal with strength\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:15		
117	BRS	33	determin\$3 adj memory adj (position\$1 or address\$2 or location\$1) and radio with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 16:59		
118	BRS	9	determin\$3 adj memory adj module\$1 with location	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:00		
119	BRS	38	memory adj module\$1 adj location	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:07		
120	BRS	13	3846765.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:07		
121	BRS	0	3846765.uref. and radio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:08		
122	BRS	8	3846765.uref. and transmit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:08		
123	BRS	497	determin\$3 adj (position\$1 or address\$2 or location\$1) adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:18		
124	BRS	21	determin\$3 adj (position\$1 or address\$2 or location\$1) adj memory with signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:20		
125	BRS	2	determin\$3 adj (position\$1 or address\$2 or location\$1) adj memory with radio with signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:27		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
126	IS&R	2	("6188879").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 17:27		
127	BRS	3	processor with signal with strength with radio with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 18:22		
128	BRS	15	processor with signal with strength with radio with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 18:22		
129	BRS	3	memory with module and circuit with board with electrical and volatile with memory and radio with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:12		
130	BRS	12	memory with module and circuit with board with electrical and volatile with memory and radio with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:13		
131	BRS	14	memory with module and circuit with board with electrical and memory and radio with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:15		
132	BRS	12	memory with module and radio with signal with strength with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:40		
133	BRS	98	memory and radio with signal with strength with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:16		
134	BRS	17	memory with module and radio with signal with strength with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:41		
135	BRS	14	memory with module and radio with ((signal or field) adj1 strength) with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 09:47		
136	BRS	161	memory and radio with ((signal or field) adj1 strength) with transmitter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 10:55		

Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
137 BRS	12	memory and radio with ((signal or field) adj strength) with transmitter and transmitters and circuit with board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 11:15		
138 BRS	87	memory and radio with ((signal or field) adj strength) with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 11:00		
139 BRS	0	(702/\$.cls.) and PCMCIA adj1 cards with electrical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 12:47		
140 BRS	54	PCMCIA adj1 cards with electrical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 12:47		
141 BRS	9	circuit with board with PCMCIA adj1 cards with electrical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 12:48		
142 BRS	14	5739791.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 13:46		
143 BRS	0	5739791.uref. and boot	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 13:46		
144 BRS	0	5739791.uref. and self-test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 13:47		
145 BRS	2	memory with module\$1 with transmit\$3 with information with self-test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 13:57		
146 BRS	7	memory with module\$1 and transmit\$3 with information with self-test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:21		
147 BRS	26	memory with module\$1 and transmit\$3 with information with (boot or self-test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:10		

Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
148 BRS	0	memory with module\$1 and radio with signal with transmitterand and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:39		
149 BRS	147	memory and radio with ((signal or field) adj1 strength) with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:40		
150 BRS	399	memory with module\$1 and radio with signal with transmitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:45		
151 BRS	2	memory with module\$1 with radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:46		
152 BRS	8	memory with module\$1 and radio with signal with transmitters and (boost\$2 or self-test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:50		
153 BRS	1	memory with radio with signal with transmitters and (boost\$2 or self-test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 14:52		
154 BRS	32	memory and radio with signal with transmitters and (boost\$2 or self-test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 15:33		
155 BRS	33	memory with module and radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 15:35		
156 BRS	2	memory with computer with radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 15:06		
157 BRS	1	memory and radio with signal with transmitters and (boost\$2 or self-test) and convent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 15:34		
158 BRS	0	memory with module and radio with signal with transmitters and convent\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 15:36		
159 BRS	119	memory with computer and radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:04		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
160	BRS	4	memory with portable with computer and radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:07		
161	BRS	44	memory and portable adjl computer and radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:19		
162	BRS	0	(710/\$.ccls.) and memory with module\$1 and computer and radio with signal with transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:22		
163	BRS	0	(710/\$.ccls. or 343/\$.ccls.) and memory with module\$1 and computer and radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:36		
164	BRS	156	memory with module\$1 and computer and radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:23		
165	BRS	75	(710/\$.ccls. or 343/\$.ccls.) and radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:37		
166	BRS	18	(710/\$.ccls. or 343/\$.ccls.) and computer and radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:42		
167	BRS	93	computer with radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:43		
168	BRS	18	memory with processor with radio with signal with transmitter and transmitters	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/04 16:44		